

**PATENT** 

## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Ryan Fung

Application No.: 10/774,857

Filed: February 9, 2004

Title: Method and Apparatus For Utilizing Long-

Path and Short-Path Timing Constraints In

An EDA Tool for Routing

Attorney Docket No. ALT.P027.1 (A1182.1)

Examiner:

Tat, Binh C

Art Unit:

2825

Mail Stop Amendment Commissioner for Patents P.O. Bos 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This Information Disclosure Statement is believed to be filed before the mailing date of a final office action on the merits under 37 CFR §1.97(c). The fee of \$180 set forth in 37 CFR §1.17(p) is being submitted herewith. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1624.

The references listed in the attached PTO Form PTO/SB/08A and 08B, may be material to examination of the above-identified patent application. Applicant submits these references in compliance with his duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

## CERTIFICATE OF MAILING

Date of Deposit May 19, 2006

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated above.

Lawrence M. Cho

(Typed a printed name of person mailing correspondence)

(Signature of person mailing correspondence)

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Serial No.: 10/774,857

ALT.P027.1 (A1182.1)

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This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If there are any additional charges, please charge them to Deposit Account No. 50-1624.

Respectfully submitted,

Dated: May 19, 2006

P.O. Box 2144 Champaign, IL 61825 Tel: (217) 377-2500

Fax: (217) 356-7373

Serial No.: 10/774,857

Lawrence M. Cho Attorney for Applicant Reg. No. 39,942

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

Complete if Known			
Application Number	10/774,857		
Filing Date	February 9, 2004		
First Named Inventor	Ryan Fung		
Art Unit	2825		
Examiner Name	Tat, Binh C		
Attorney Docket Number	ALT.P027.1 (A1182.1)		

Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		<sup>US-</sup> 6,826,773	11-2004	Hathaway et al.	
		<sup>US-</sup> 6,763,506	07-2004	Betz et al.	
		<sup>US-</sup> 6,708,139	03-2004	Rearick et al.	
		<sup>US-</sup> 5,659,484	08-1997	Bennett et al.	
		<sup>US-</sup> 5,541,849	07-1996	Rostoker et al.	
		<sup>US-</sup> 5,237,514	08-1993	Curtin, James J.	
		<sup>US-</sup> 5,521,837	05-1996	Frankle et al.	
140		US- 2005/0251775	11-2005	Wood, John	
		<sup>US-</sup> 2005/0138578	06-2005	Alpert et al.	
		US- 2005/0132318	06-2005	Kidd et al.	
		<sup>US-</sup> 2005/0039156	02-2005	Catthoor et al.	
		<sup>US-</sup> 2004/0243964	12-2004	McElvain et al.	
		<sup>US-</sup> 2003/0005398	01-2003	Cho et al.	
		US-			

	FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	<b>-6</b>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	MM-DD-YYYY		Or Relevant Figures Appear	
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Examiner Signature	Date Considered	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at <a href="https://www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449/PTO	re required to respond to a collection of information unless it contains a valid OMB control number.  Complete if Known		
Substitute for form 1440/170	Application Number	10/774,857	
INFORMATION DISCLOSURE	Filing Date	February 9, 2004	
STATEMENT BY APPLICANT	First Named Inventor	Ryan Fung	
(Use as many sheets as necessary)	Art Unit	2825	
(Use as many sneets as necessary)	Examiner Name	Tat, Binh C	
Sheet 2 of 2	Attorney Docket Number	ALT.P027.1 (A1182.1)	

Examiner Initials* No.¹ Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.  NN9107410, "Linear Algorithm for Distributing Slack So That All Nets of a Design Have a Zero Slack Pin", IBM Technical Disclosure Bulletin, Vol. 34, No. 2, July 1991, pp. 410-414.  Mathur et al., "Timing-Driven Placement for Regular Architecures*, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 16, No. 6, June 1997, pp. 597-608.		NON PATENT LITERATURE DOCUMENTS	
Technical Disclosure Bulletin, Vol. 34, No. 2, July 1991, pp. 410-414.  Mathur et al., "Timing-Driven Placement for Regular Architecures", IEEE Transactions on Computer-Aided	 Cite No.1	the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue	T <sup>2</sup>
Mathur et al., "Timing-Driven Placement for Regular Architecures", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 16, No. 6, June 1997, pp. 597-608.		NN9107410, "Linear Algorithm for Distributing Slack So That All Nets of a Design Have a Zero Slack Pin", IBM Technical Disclosure Bulletin, Vol. 34, No. 2, July 1991, pp. 410-414.	
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